

AMENDMENTS TO THE CLAIMS

1. **(Currently Amended)** A packet processing device for processing packets in which variable length data is split and stored, the packet processing device comprising:

a header analyzing section for analyzing a header of an inputted packet and determining whether data stored in a payload is start data containing start information or other data;

a data extracting section for referring to an analysis result from the header analyzing section, and extracting the data from the payload of the packet;

a buffer for storing the data extracted by the data extracting section;

a buffer controlling section for controlling a stored position and an accumulated data amount of the data in the buffer; ~~and~~

a start data identifying section for generating information for identifying the start data in the buffer, based on the analysis result from the header analyzing section and the control by the buffer controlling section; and

a decode section for reading out data from the buffer with a predetermined timing, and for performing a decode process for the data read out based on the information for identifying the start data in the buffer, the information having been generated by the start data identifying section.

2. **(Original)** The packet processing device according to claim 1,

wherein the start data identifying section comprises:

a start position memory for retaining stored position information of the start data stored in the buffer; and

a number-of-starts counter for counting a number of the start data stored in the buffer.

3. **(Original)** The packet processing device according to claim 2,
wherein the start position memory is a register for retaining the stored position information.

4. **(Original)** The packet processing device according to claim 2,
wherein the start position memory is a memory, constructed independently of the buffer,
for retaining the stored position information.

5. **(Original)** The packet processing device according to claim 2,
wherein the stored position information is a write address in the buffer storing the start data.

6. **(Original)** The packet processing device according to claim 2,
wherein the stored position information is information representing a position of the start data relative to first data stored in the buffer.

7. **(Previously Presented)** The packet processing device according to claim 2,
wherein the buffer controlling section compares the accumulated data amount against a predetermined threshold amount and, when the accumulated data amount becomes equal to or greater than the threshold amount, outputs a predetermined notification signal.

8. **(Original)** The packet processing device according to claim 7,
wherein, when detecting the predetermined notification signal, the number-of-starts counter displays a number of the start data contained in an amount of data corresponding to the threshold amount.

9. **(Previously Presented)** The packet processing device according to claim 2,
wherein the buffer controlling section compares the number in the number-of-starts counter against a predetermined threshold number and, when the number becomes equal to or greater than the threshold number, outputs a predetermined notification signal.

10. **(Original)** The packet processing device according to claim 7,
wherein the threshold number is a number of areas in the start position memory which enables retention of the stored position information.

11. **(Currently Amended)** The packet processing device according to claim 2, ~~further comprising:~~

[[a]] ~~wherein the decode section for reading reads~~ out data from the buffer with [[a]] ~~the~~ predetermined timing, ~~obtaining obtains~~ stored position information and a count number from the start data identifying section, ~~separating separates~~ the start data contained in the data read out based on the stored position information and the count number into start information and data, and ~~performing a performs~~ the decode process for the data read out based on the start

information.

12. **(Original)** The packet processing device according to claim 11,

wherein the buffer controlling section compares the accumulated data amount against a predetermined threshold amount, and, when the accumulated data amount becomes equal to or greater than the threshold amount, outputs a predetermined notification signal,

wherein, when detecting the predetermined notification signal, the number-of-starts counter displays a number of the start data contained in an amount of data corresponding to the threshold amount, and

wherein the decode section reads out an amount of data corresponding to the threshold amount from the buffer at a timing of receiving the notification signal.

13. **(Original)** The packet processing device according to claim 11,

wherein the buffer controlling section compares the number in the number-of-starts counter against a predetermined threshold number, and, when the number becomes equal to or greater than the threshold number, outputs a predetermined notification signal, and

wherein the decode section reads out the data from the buffer at a timing of receiving the notification signal.

14. **(Original)** The packet processing device according to claim 1, wherein, when a transport stream packet under MPEG technique that stores variable-length PES data is inputted, the header analyzing section analyzes a header of a TS packet in the inputted transport

stream, and determines whether data stored in a payload is start PES data containing a PES header or other PES data,

the data extracting section refers to an analysis result from the header analyzing section, and extracts PES data from the payload of the TS packet,

the buffer stores the PES data extracted by the data extracting section,

the buffer controlling section controls a stored position and an accumulated data amount of the PES data in the buffer, and

the start data identifying section generates information for identifying the start PES data in the buffer, based on the analysis result from the header analyzing section and the control by the buffer controlling section.

15. (Currently Amended) A packet processing method for processing packets in which variable length data is split and stored, the packet processing method comprising:

an analysis step of analyzing a header of an inputted packet, and determines whether data stored in a payload is start data containing start information or other data;

an extracting step of referring to an analysis result from the analyzing step, and extracting the data from the payload of the packet;

a step of storing the data extracted in the extracting step to a buffer;

a control step of controlling a stored position and an accumulated data amount of the data in the buffer; and

an identifying step of generating information for identifying the start data in the buffer, based on the analysis result from the analyzing step and the control from the control step;

a reading step of reading out data from the buffer with a predetermined timing;
a separating step of separating, based on the information for identifying the start data, the
start data contained in the data read out into start information and data; and
a decoding step of performing a decode process for the data read out based on the start
information.

16-18. (Canceled)

19. (Currently Amended) A computer readable medium having recorded thereon a computer readable program for causing a computer to execute a packet processing method for processing packets in which variable length data is split and stored, the packet processing method comprising:

~~wherein the program recorded in the medium causes the computer to execute:~~
an analysis step of analyzing a header of an inputted packet, and determining whether data stored in a payload is start data containing start information or other data;
an extracting step of referring to an analysis result from the analyzing step, and extracting the data from the payload of the packet;
a step of storing the data extracted in the extracting step to a buffer;
a control step of controlling a stored position and an accumulated data amount of the data in the buffer; ~~and~~
an identifying step of generating information for identifying the start data in the buffer, based on the analysis result from the analyzing step and the control from the control step;

a reading step of reading out data from the buffer with a predetermined timing;
a separating step of separating, based on the information for identifying the start data, the
start data contained in the data read out into start information and data; and
a decoding step of performing a decode process for the data read out based on the start
information.

20. (Canceled)

21. (Currently Amended) An integrated circuit to be incorporated into a device which processes packets in which variable length data is split and stored,

wherein the device comprises a buffer for storing data, and

wherein the integrated circuit integrates circuitry functioning as:

a header analyzing section for analyzing a header of an inputted packet, and determining whether data stored in a payload is start data containing start information or other data;

a data extracting section for referring to an analysis result from the header analyzing section, and extracting the data from the payload in the packet;

a buffer for storing the data extracted by the data extracting section;

a buffer controlling section for controlling a stored position and an accumulated data amount of the data in the buffer; ~~and~~

a start data identifying section for generating information for identifying the start data in the buffer, based on the analysis result from the header analyzing section and the control by the buffer controlling section; and

a decode section for reading out data from the buffer with a predetermined timing, and for performing a decode process for the data read out based on the information for identifying the start data in the buffer, the information having been generated by the start data identifying section.

22. **(Currently Amended)** The integrated circuit according to claim 21, ~~further integrates circuitry functioning as-~~

[[a]] wherein the decode section ~~for reading~~ reads out data from the buffer with [[a]] the predetermined timing, ~~obtaining~~ obtains information for identifying the start data from the start data identifying section, ~~separating~~ separates the start data contained in the data read out based on the stored position information and the count number into start information and data, and ~~performing a~~ performs the decode process for the data read out based on the start information.